

FIG. 1

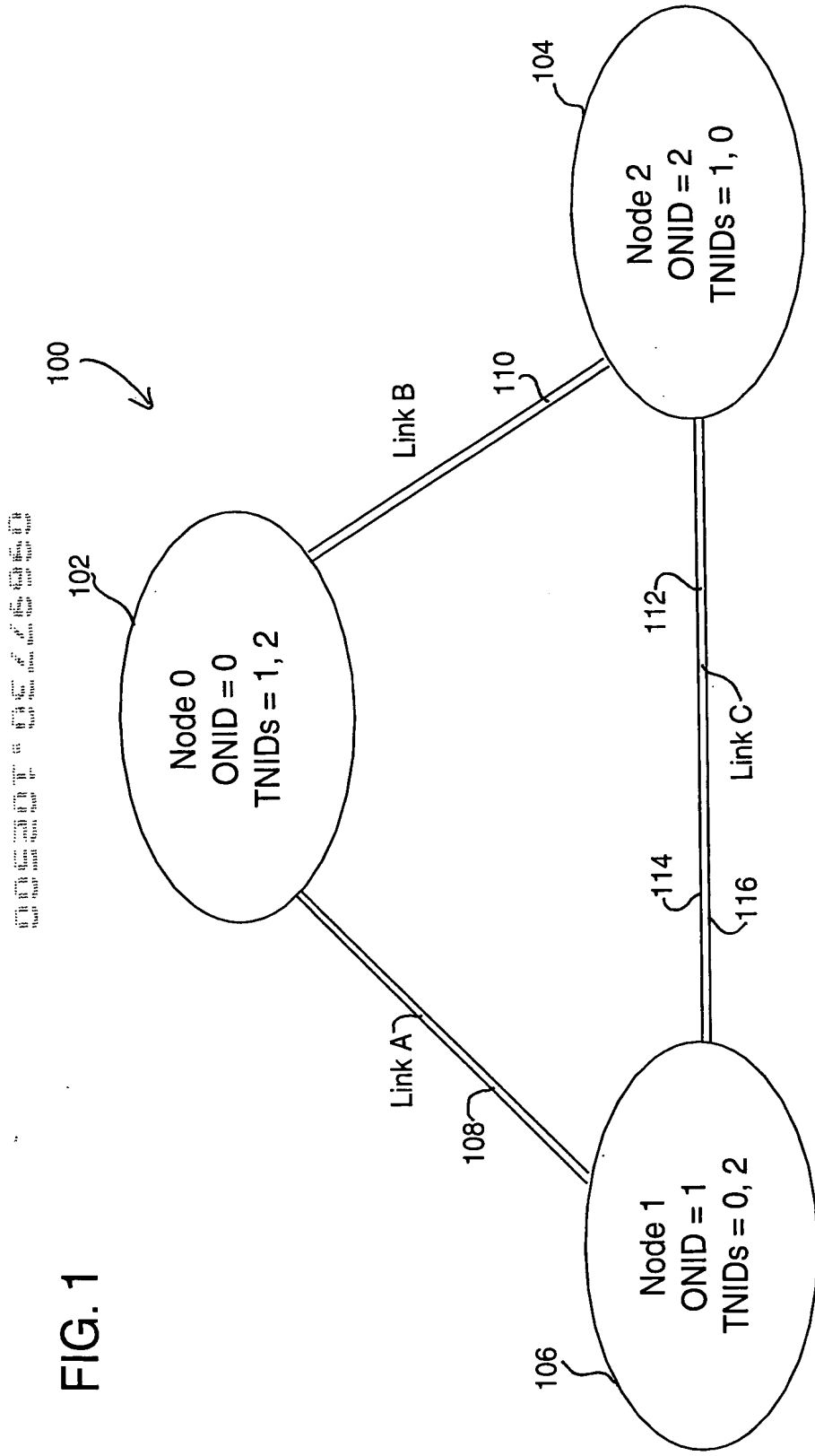


FIG. 2

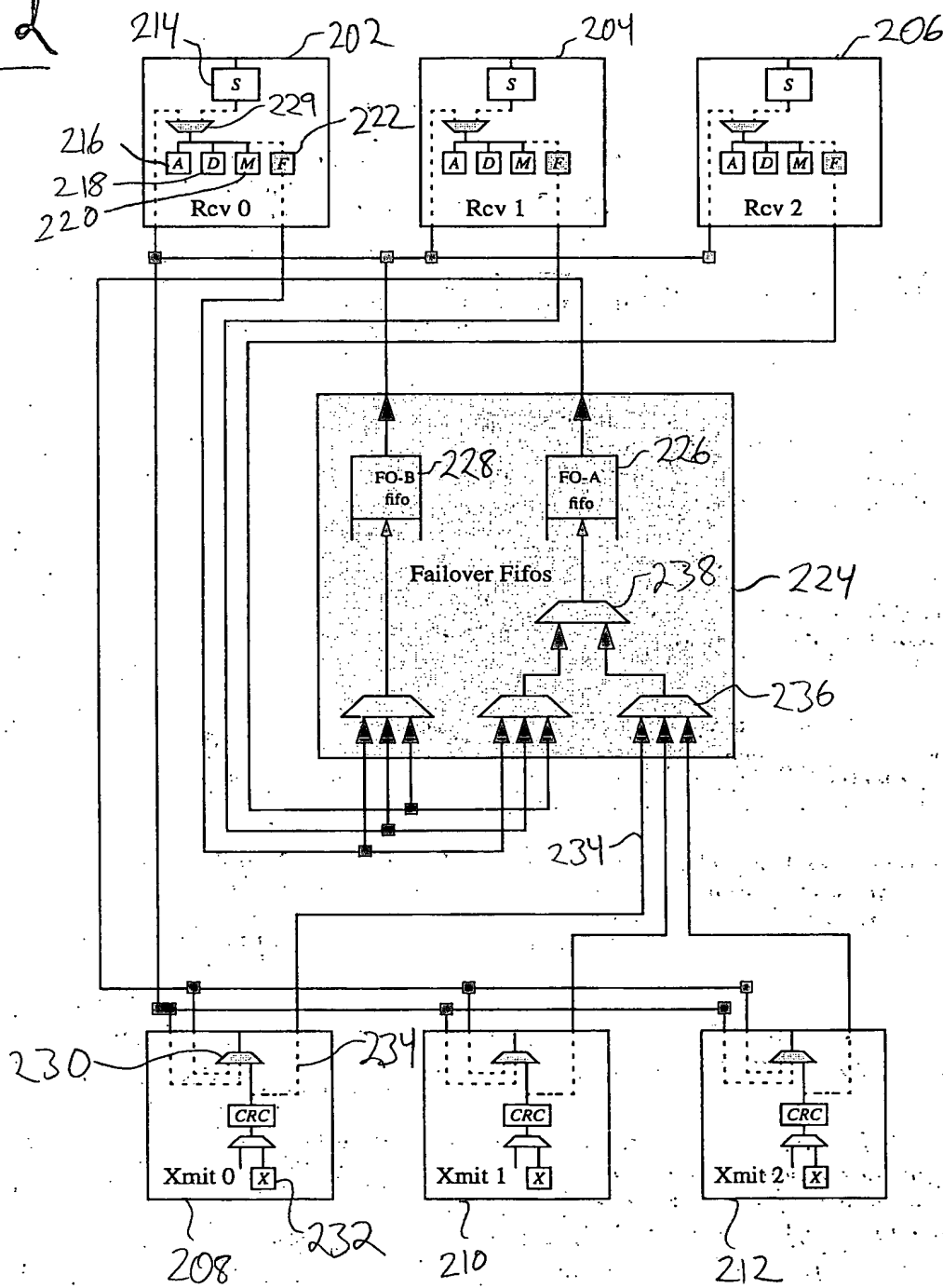


FIG. 3A

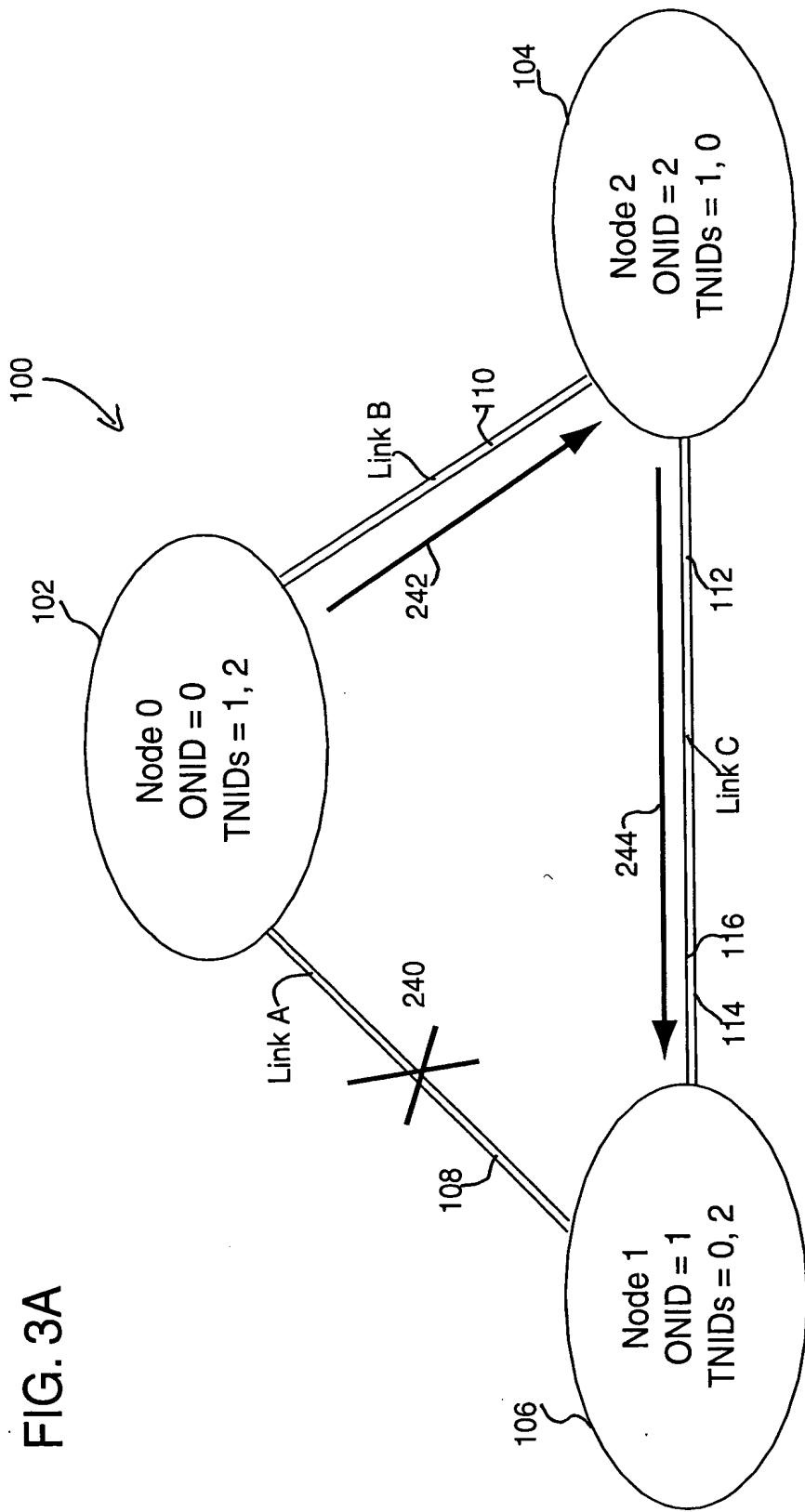


FIG. 3B

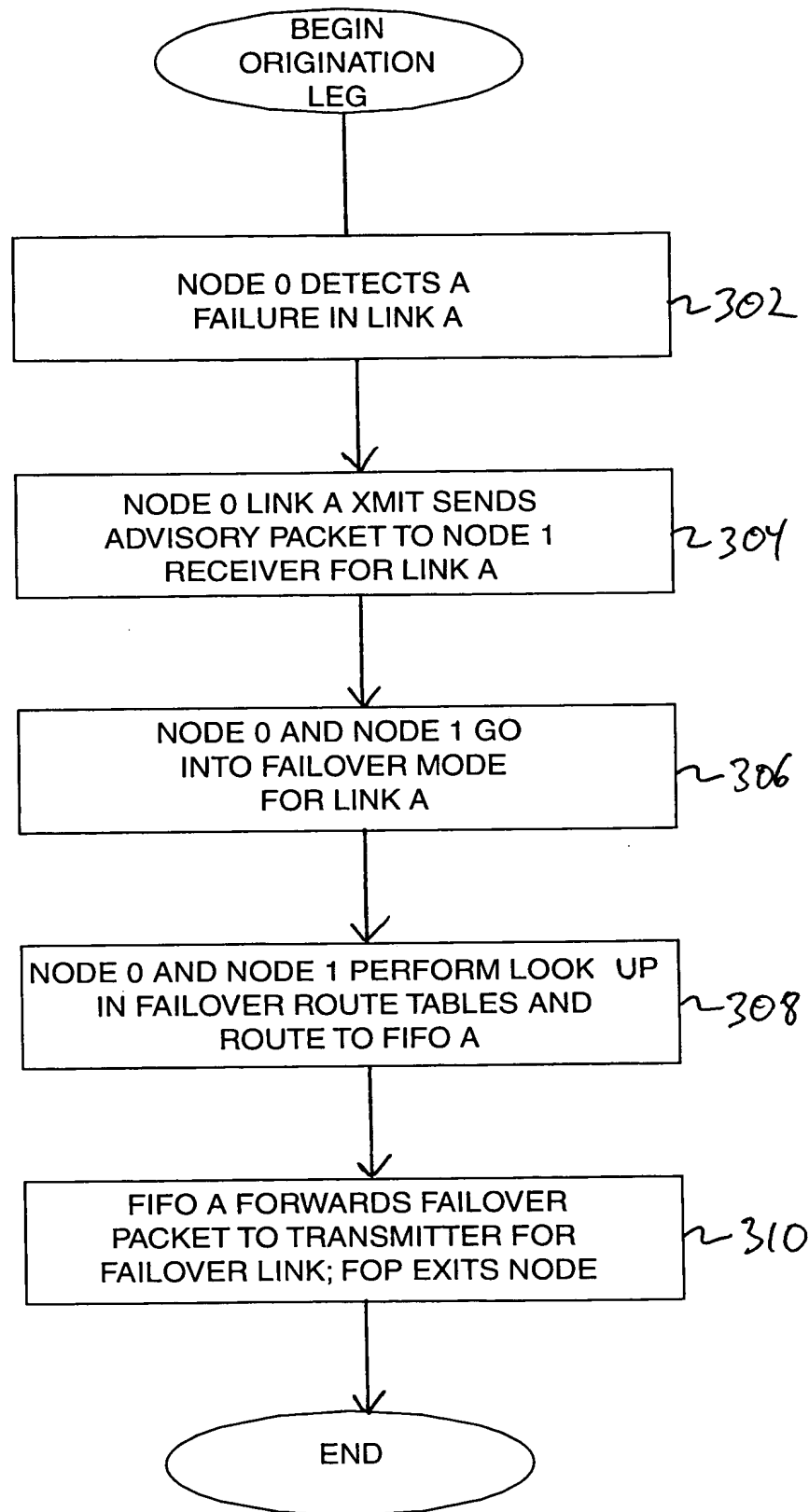


FIG. 4

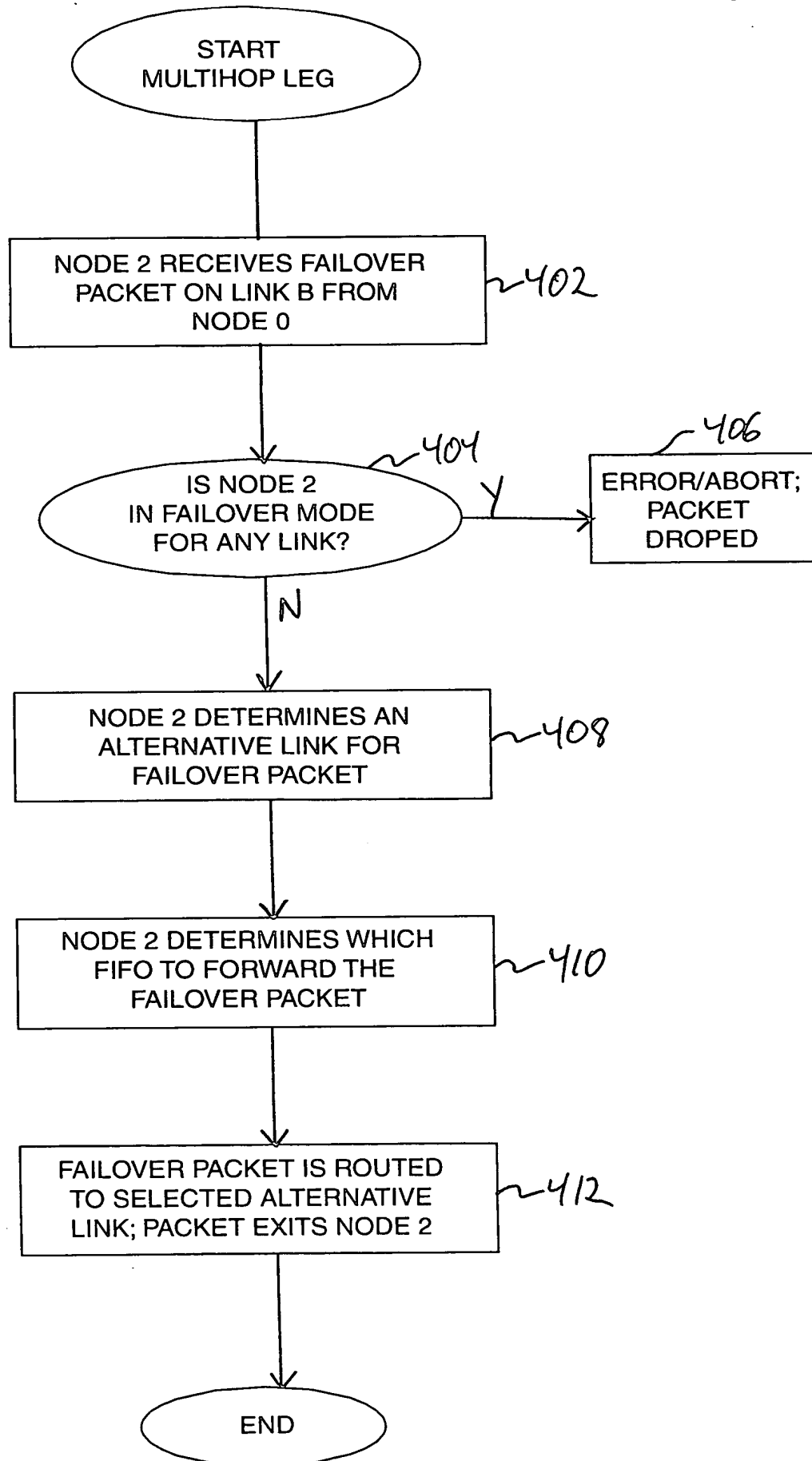


FIG. 5

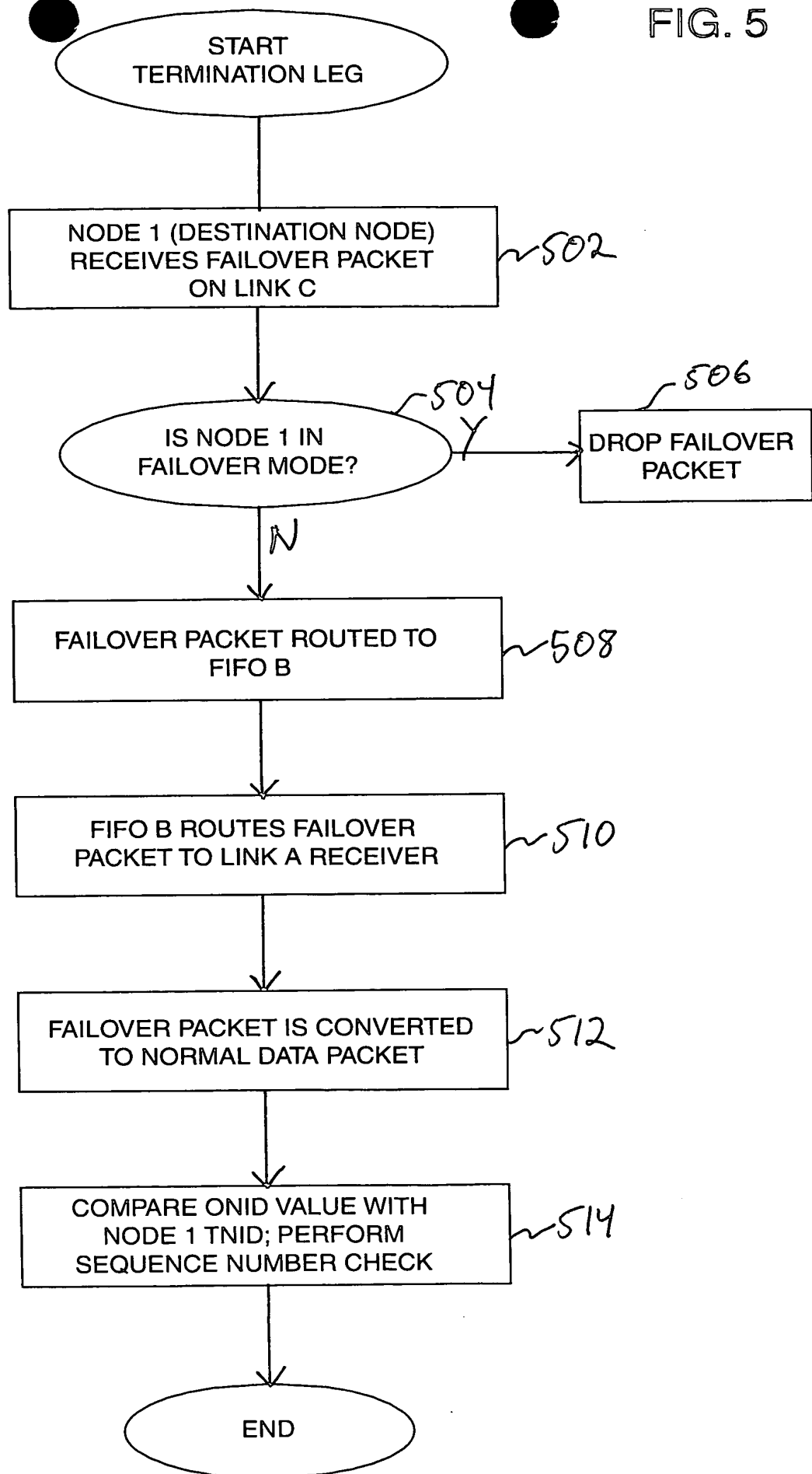


FIG. 6

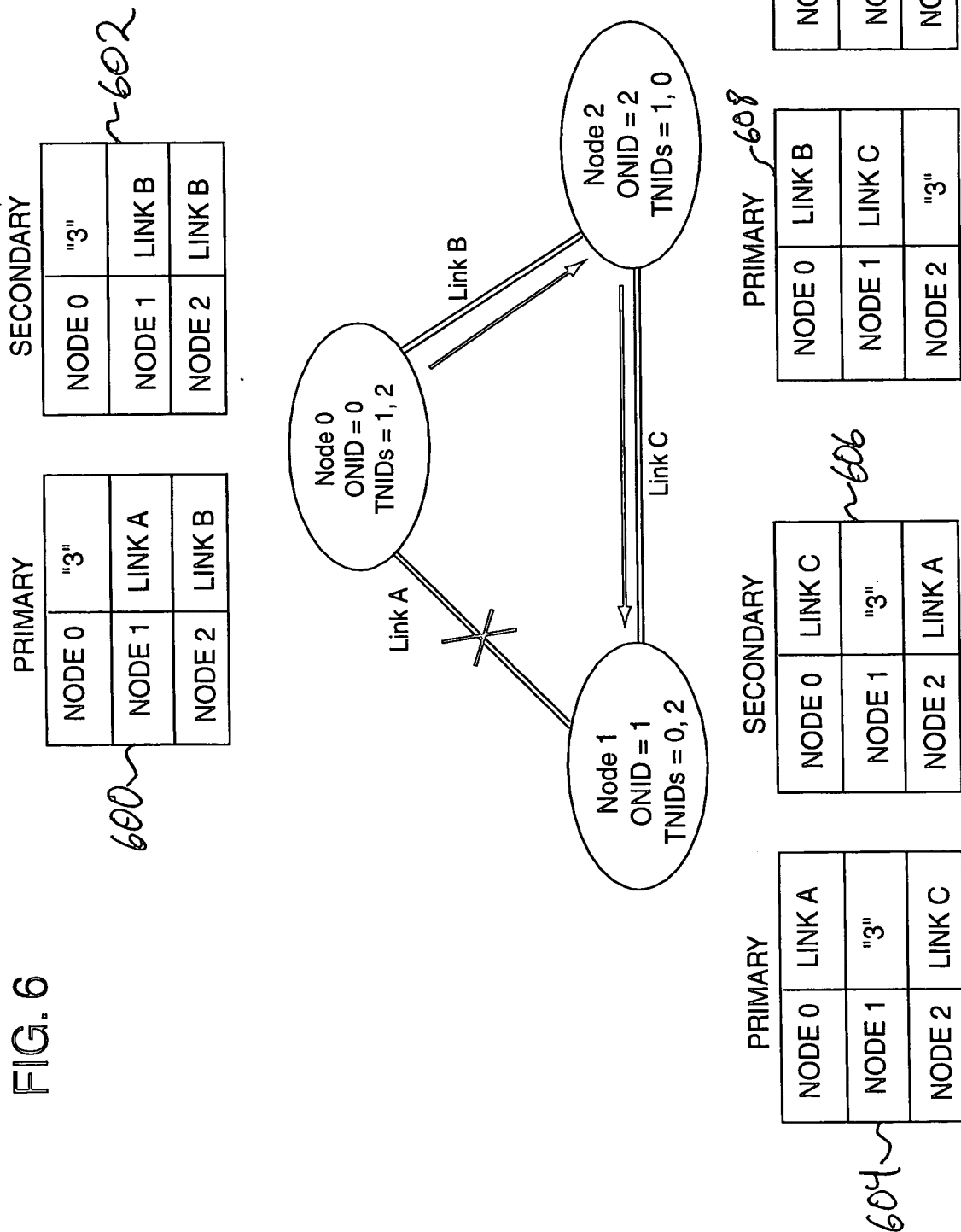


FIG. 7

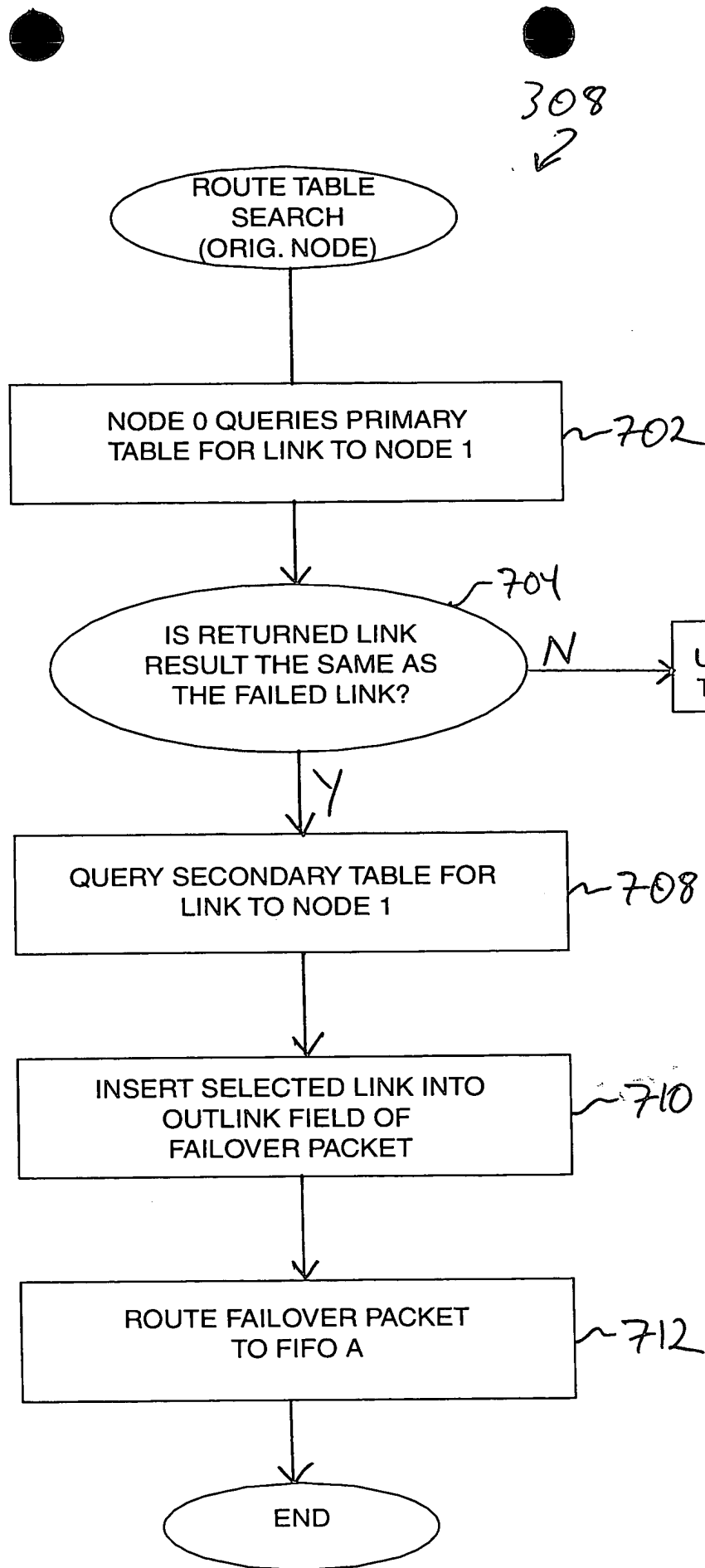


FIG. 8

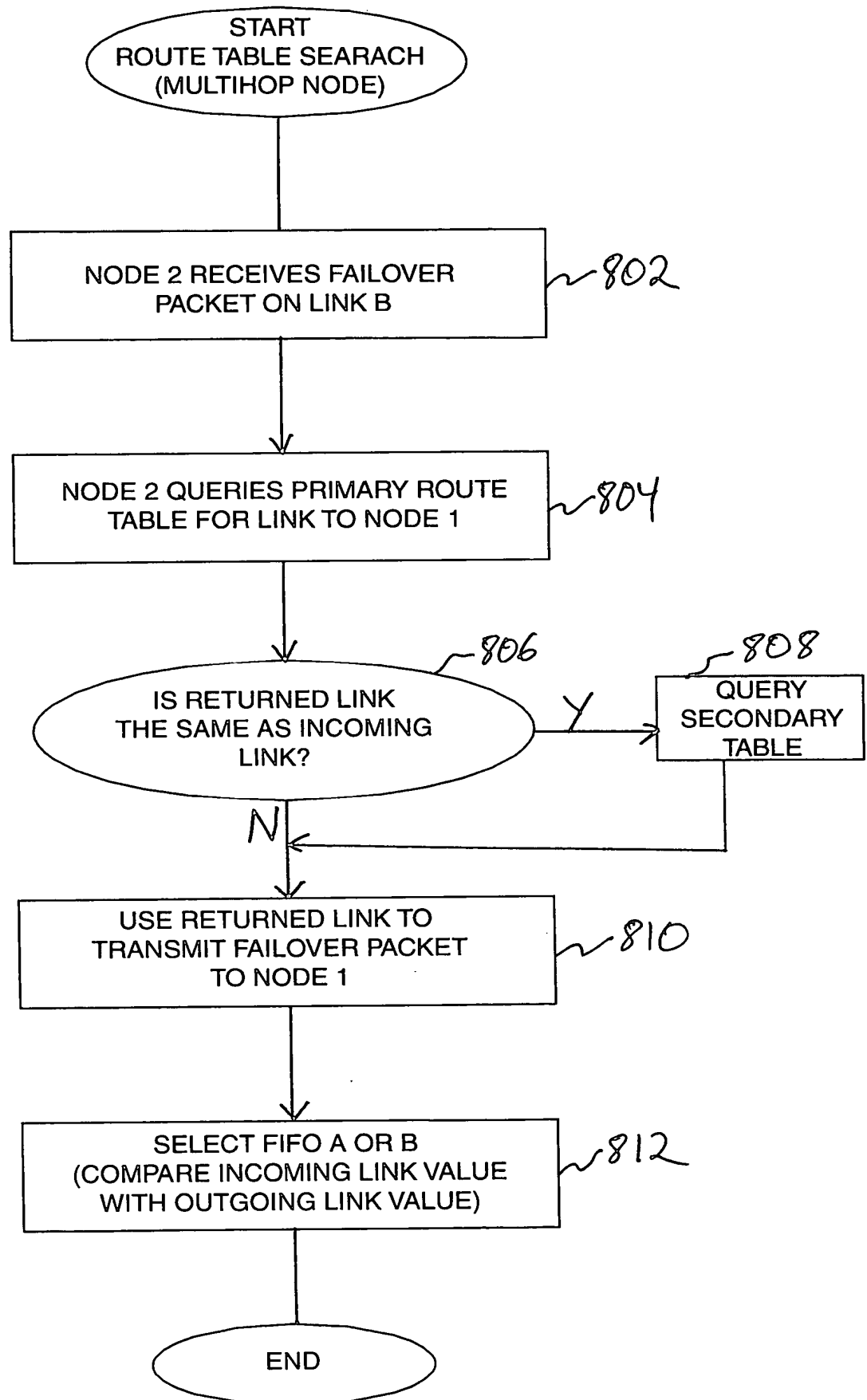


FIG. 9

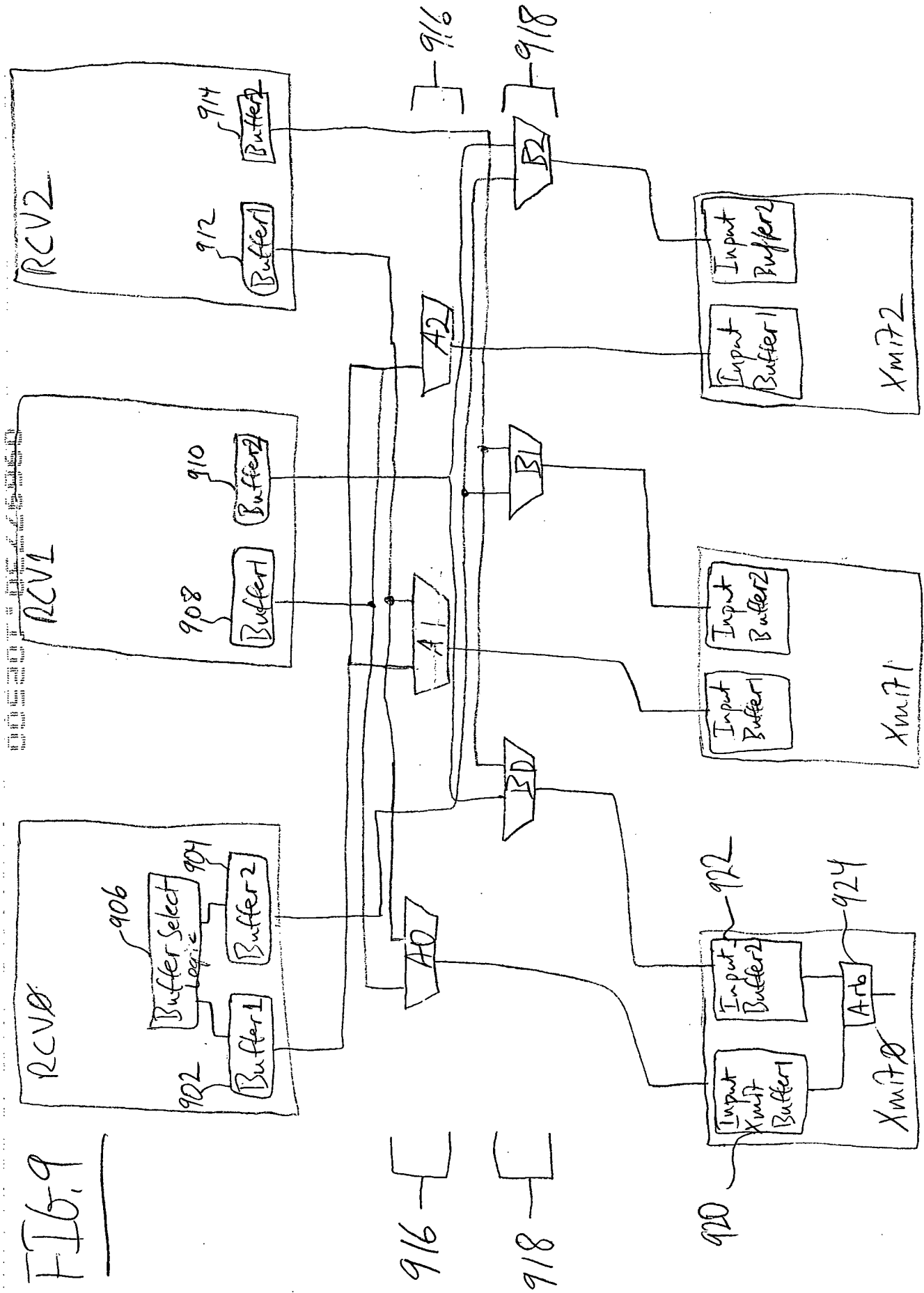
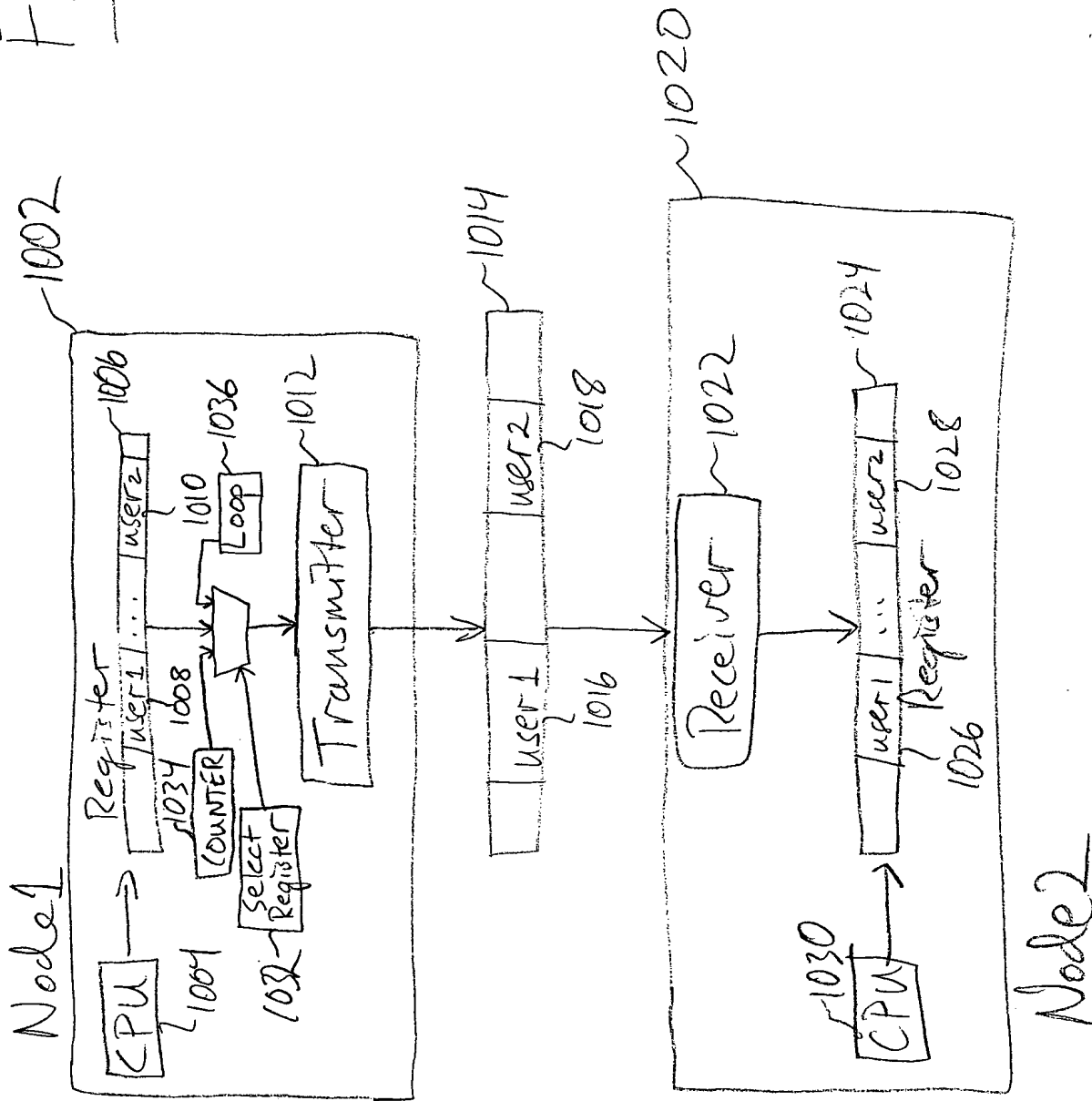


FIG. 10



Link Calibration

FIG. 11A

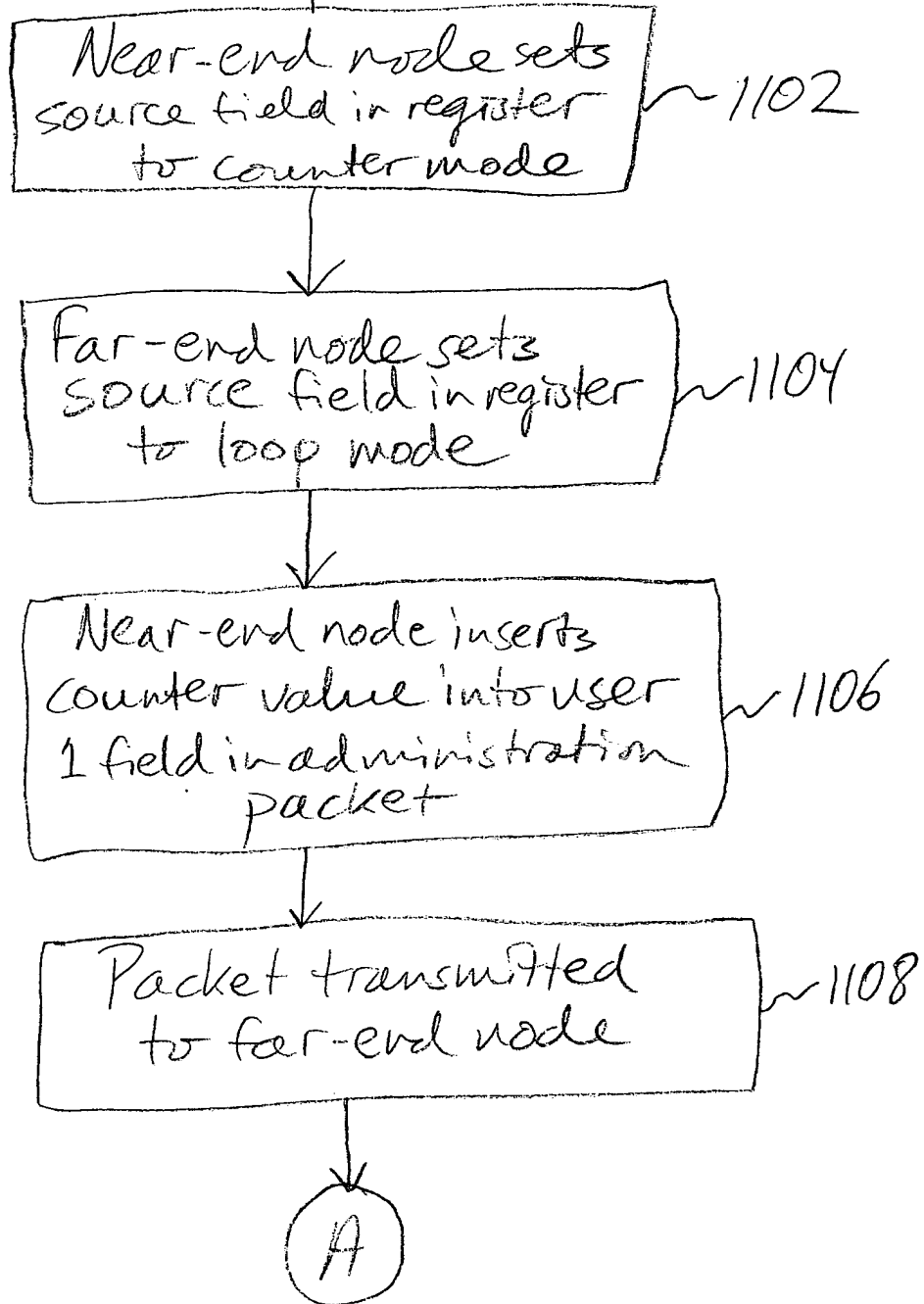


FIG. 11B

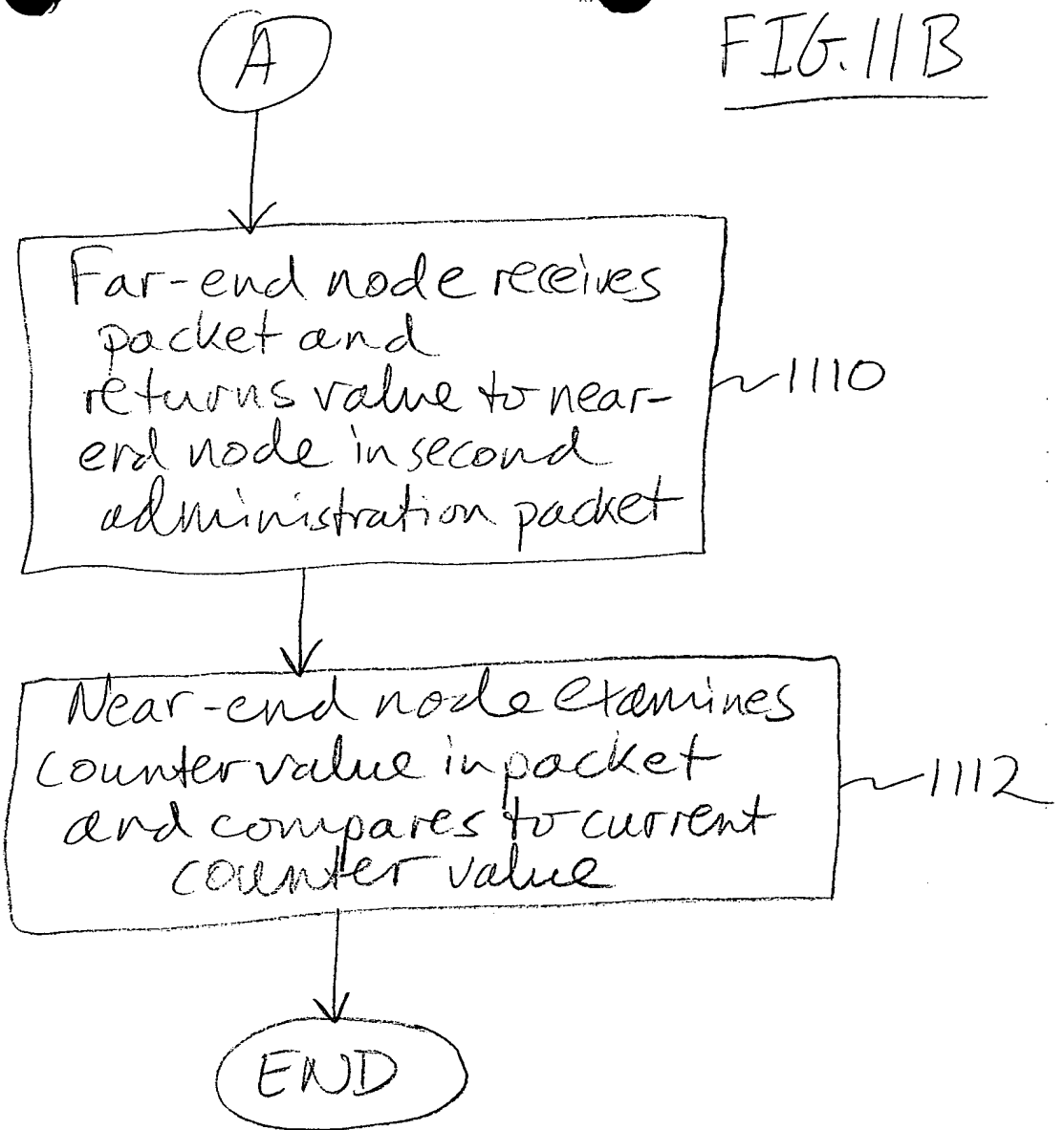


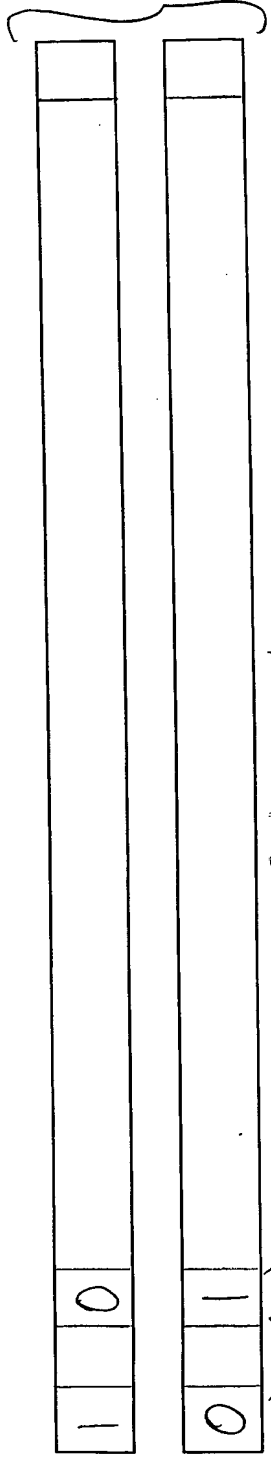
FIG. 12

FIG. 12 is a schematic diagram of a packet structure.

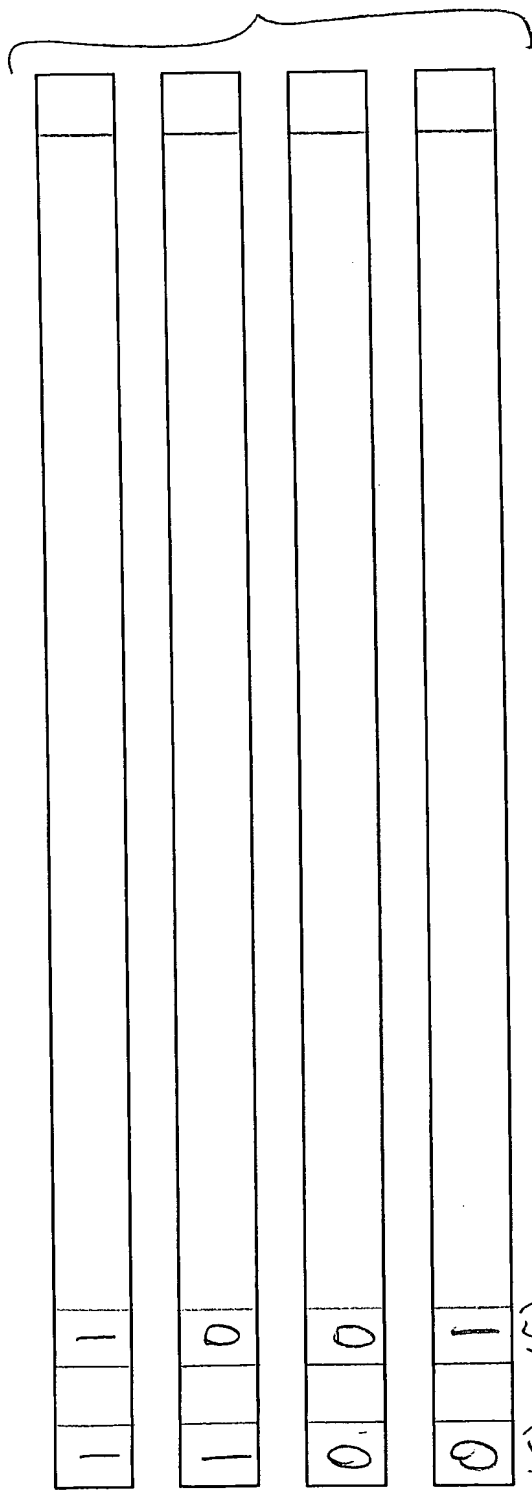
Bits: 87 85 ... 120 ... 0



One-Flit Packet



Two-Flit Packet



Four-Flit Packet